

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	Confirmation No.: 3838
Hiroki Goko)	
Application No. 10/813,031)	Examiner: MEMULA, Suresh
Filed: March 31, 2004)	Group Art Unit: 2825
For: APPARATUS AND METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT)	Date: June 13, 2008

AMENDMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is responsive to the Office Action dated April 7, 2008 in the above application. In view of the following amendments and remarks, reconsideration of the application is respectfully requested.

Amendments to the Claims

The claims are amended as shown on the following pages under the heading "LIST OF CURRENT CLAIMS". The list shows the status of all claims presently in the application including any current amendments. This list of claims is intended to supersede all prior listing(s) of the claims in the application. Any cancellation of claims is made without prejudice or disclaimer.